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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR

(AUTONOMOUS)

B.Tech III Year II Semester Supplementary Examinations Dec 2019

OBJECT ORIENTED ANALYSIS AND DESIGN

(Computer Science & Engineering)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

- 1 a Describe the conceptual model of the UML? 8M
b Explain the relationships in the UML. 4M

OR

- 2 a Explain the Modeling system architecture of UML? 7M
b Describe software development life cycle with suitable example? 5M

UNIT-II

- 3 Briefly explain about structural and behavioral diagrams and its modeling techniques 6M

OR

- 4 Describe the usage of advanced relationships used in modeling 6M

UNIT-III

- 5 a Describe the Common Modeling Techniques for class diagram?. 7M
b Difference between sequence and collaboration diagram 5M

OR

- 6 a Describe the Common Modeling Techniques for Use case diagram? 7M
b Explain collaboration diagram with an example? 5M

UNIT-IV

- 7 a Briefly describe the terms and concepts of events? 7M
b Explain the term and concepts of processes and threads 5M

OR

- 8 a Explain the Advanced States and Transitions. 6M
b Draw a state chart diagram for the ATM system. 6M

UNIT-V

- 9 a Define deployment. Explain the modeling techniques of deployments. 7M
b Draw a component diagram for the college management system. 5M

OR

- 10 a Briefly explain Standard Elements in Components? 7M
b Explain the common modeling techniques for deployment diagram. 5M

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